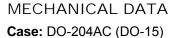


# **Glass Passivated High Efficient Rectifiers**

#### **FEATURES**

- Glass passivated chip junction
- High efficiency, Low VF
- High current capability
- High surge current capability
- Low power loss
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



Molding compound, UL flammability classification rating 94V-0

Moduling compound, or naminability classification rating 544-0

Base P/N with suffix "G" on packing code - green compound (halogen-free)

Base P/N with prefix "H" on packing code - AEC-Q101 qualified **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 1A whisker test

with prefix "H" on packing code meet JESD 201 class 2 whisker test

Weight: 0.4g (approximately)



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T <sub>A</sub> =25°C unless otherwise noted)										
PARAMETER	SYMBOL	HER 201G	HER 202G	HER 203G	HER 204G	HER 205G	HER 206G	HER 207G	HER 208G	UNIT
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	200	300	400	600	800	1000	V
Maximum RMS voltage		35	70	140	210	280	420	560	700	V
Maximum DC blocking voltage	$V_{DC}$	50	100	200	300	400	600	800	1000	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	2						Α		
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60					Α			
Maximum instantaneous forward voltage (Note 1) @ 2 A	V <sub>F</sub>	1.0 1.3			1.7		V			
Maximum reverse current @ rated VR $T_J$ =25 $^{\circ}$ C $T_J$ =125 $^{\circ}$ C	I <sub>R</sub>	5 150					μΑ			
Maximum reverse recovery time (Note 2)	Trr	50 75			ns					
Typical junction capacitance (Note 3)	Cj	35 20			pF					
Typical thermal resistance	$R_{\theta JA}$	60		°C/W						
Operating junction temperature range	T <sub>J</sub>	- 55 to +150			оС					
Storage temperature range	T <sub>STG</sub>	- 55 to +150			оС					

Note 1: Pulse test with PW=300 µs, 1% duty cycle

Note 2: Reverse Recovery Test Conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.

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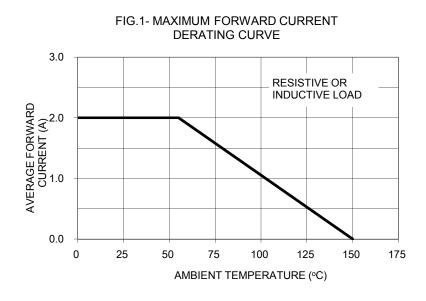
ORDERING INFORMATION						
PART NO.	AEC-Q101	PACKING CODE	GREEN COMPOUND	PACKAGE	PACKING	
	QUALIFIED		CODE			
LIEDOOO		A0		DO-15	1,500 / Ammo box	
HER20xG (Note 1)	Prefix "H"	R0	Suffix "G"	DO-15	3,500 / 13" Paper reel	
		B0		DO-15	1,000 / Bulk packing	

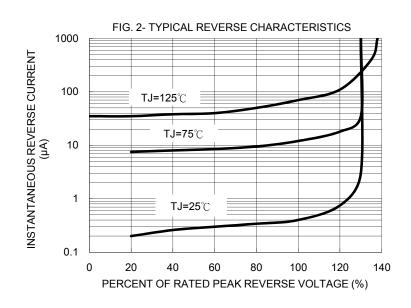
Note 1: "x" defines voltage from 50V (HER201G) to 1000V (HER208G)

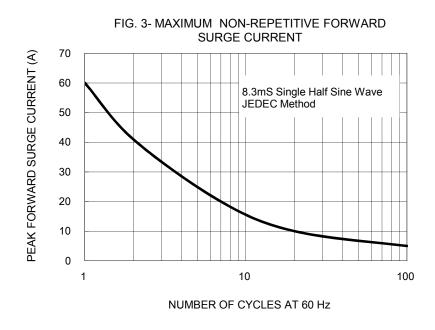
EXAMPLE							
PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION		
HER201G A0	HER201G		A0				
HER201G A0G	HER201G		A0	G	Green compound		
HER201GHA0	HER201G	Н	A0		AEC-Q101 qualified		

#### RATINGS AND CHARACTERISTICS CURVES

(TA=25°C unless otherwise noted)







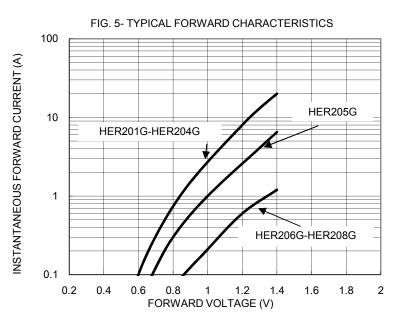


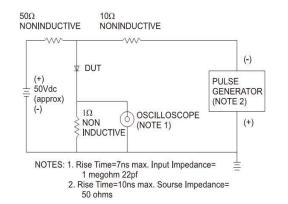


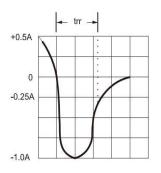
FIG. 5- TYPICAL JUNCTION CAPACITANCE

175
150
(150)
125
100
HER201G-HER205G
0
0.1
1
1
10
100
1000

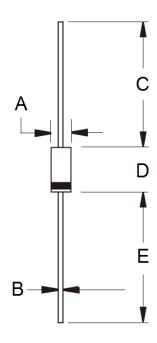
REVERSE VOLTAGE (V)

#### FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





## PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)			
	Min	Max	Min	Max		
Α	2.60	3.60	0.102	0.142		
В	0.70	0.90	0.028	0.035		
С	25.40	-	1.000	-		
D	5.80	7.60	0.228	0.299		
Е	25.40	-	1.000	-		

### MARKING DIAGRAM



P/N = Specific Device Code G = Green Compound YWW = Date Code

F = Factory Code





Taiwan Semiconductor

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